carbon design systems

News Release

Waltham, MA,

Carbon Continues European Expansion, Adds Distributors in Germany & Scandinavia; SoC Virtual System Prototyping Key to Success

TRIAS Mikroelektronik (Germany) and ISS Group (Sweden) Supports Carbon's Expansion

WALTHAM, MA, March 2, 2006, Carbon Design Systems, a leader in virtual system prototyping, announced today that TRIAS Mikroelektronik GmbH, based in Germany and the ISS Group, based in Sweden, have signed-up to distribute and support Carbon's VSP™ and SOC-VSP™ software products. TRIAS territory includes Germany, Austria, Switzerland, and Romania. ISS -- The Scandinavian Gateway -- targets Sweden, Norway, Denmark, Finland, Estonia, Poland, Latvia, and Lithuania. These additions round out Carbon's European store-front, after recently announcing Saros Technology Limited as its United Kingdom, France, and Netherlands distributor.

"In establishing its European presence, Carbon focused on successful distributors who sell validation tools to ESL architects, firmware developers, and hardware designers," declared Alan Swahn, Vice President of Marketing and Business Development at Carbon Design Systems. "Our VSP product line is a great addition to their line-cards and creates opportunities to sell to an under-served ESL community that has been waiting for a solution that allows IP import and leverages RTL throughout the design cycle for profiling, early architecture validation, and firmware development."

Carbon's SoC Virtual Prototyping Software

Carbon's SOC-VSP is the first virtual system prototyping solution that combines the power of ARM's RealView® SoC Designer and Carbon's ability to incorporate a design's RTL -- Verilog® and VHDL. This combination allows a hardware-accurate, soft-model of a SoC to be rapidly assembled and functionally validated on an engineer's desktop. The hardware view is provided by "Carbonizing" a design's RTL that results in a high-speed, transaction-level component that can be profiled, probed, debugged, and validated as part of RealView simulation. The chip's architecture, software content, and hardware implementation can all be wrung-out before first silicon.

About Carbon

Carbon's products in conjunction with SystemC™ simulation platforms, such as ARM® RealView® SoC Designer, enable the first effective system validation solution. Carbon dramatically reduces design cycle time by allowing legacy IP and RTL to be used early for architecture profiling and tradeoff analysis all the way through software and hardware integration. Unlike the time-sink to create behavioral models or bring-up an expensive emulator, Carbon generates high-performance hardware-accurate simulation models from RTL in minutes.

Carbon's new approach accelerates time-to-profit by enabling software validation to occur in parallel with hardware development. Problems can be found and resolved early in the design

cycle-rather than waiting for FPGA prototypes to be built or silicon to be delivered.

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